

FIG. 1

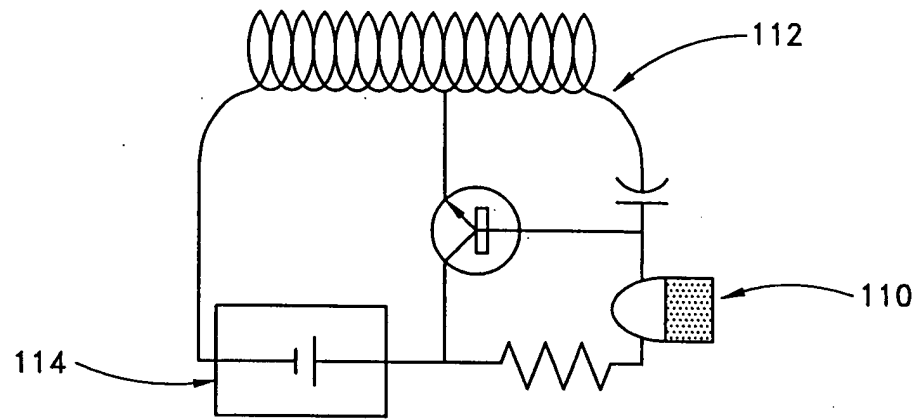


FIG. 2

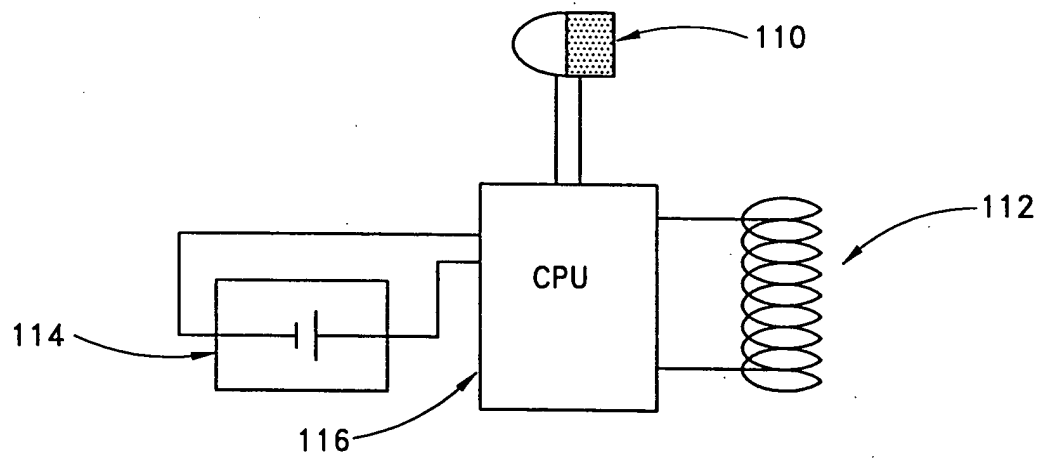


FIG. 3

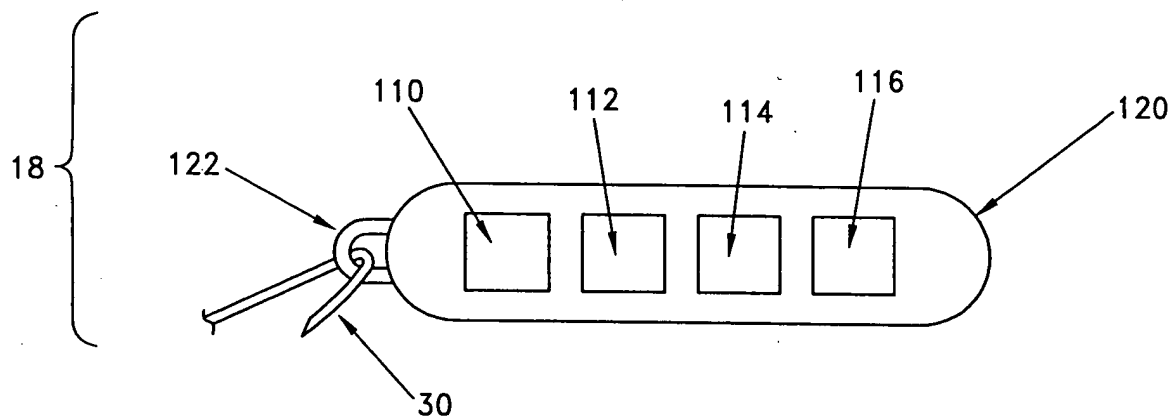
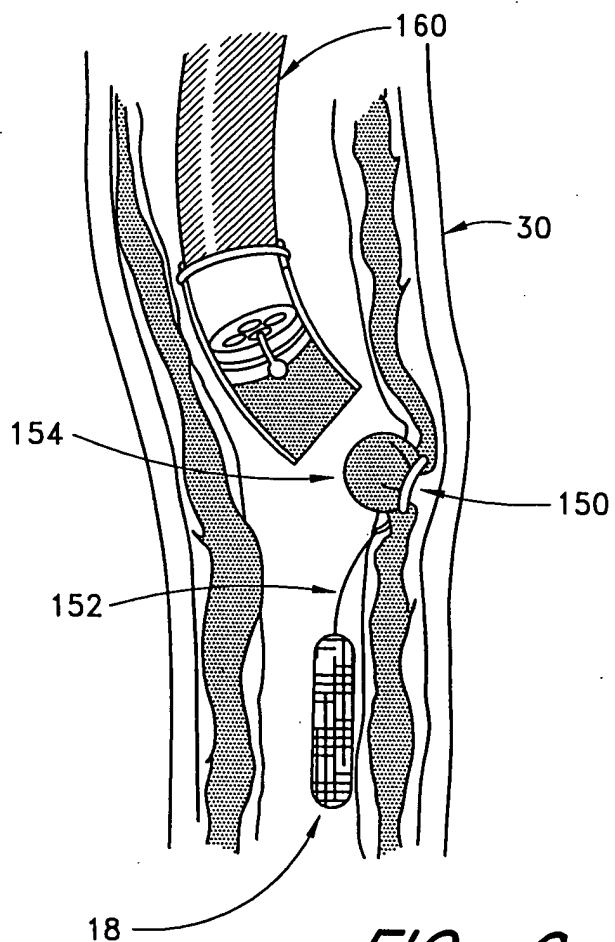
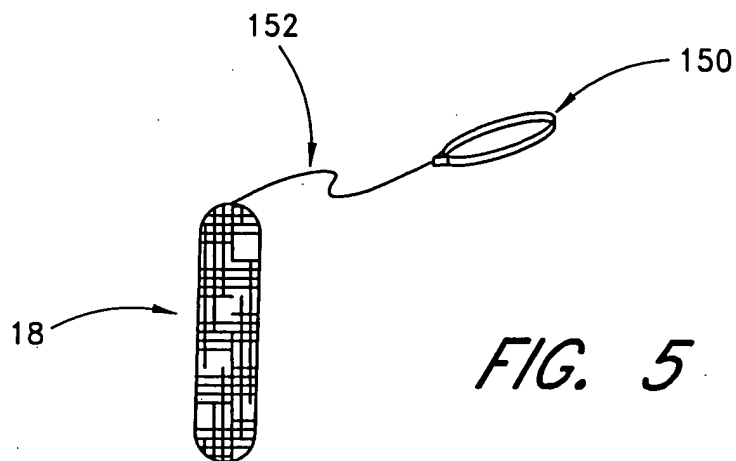


FIG. 4



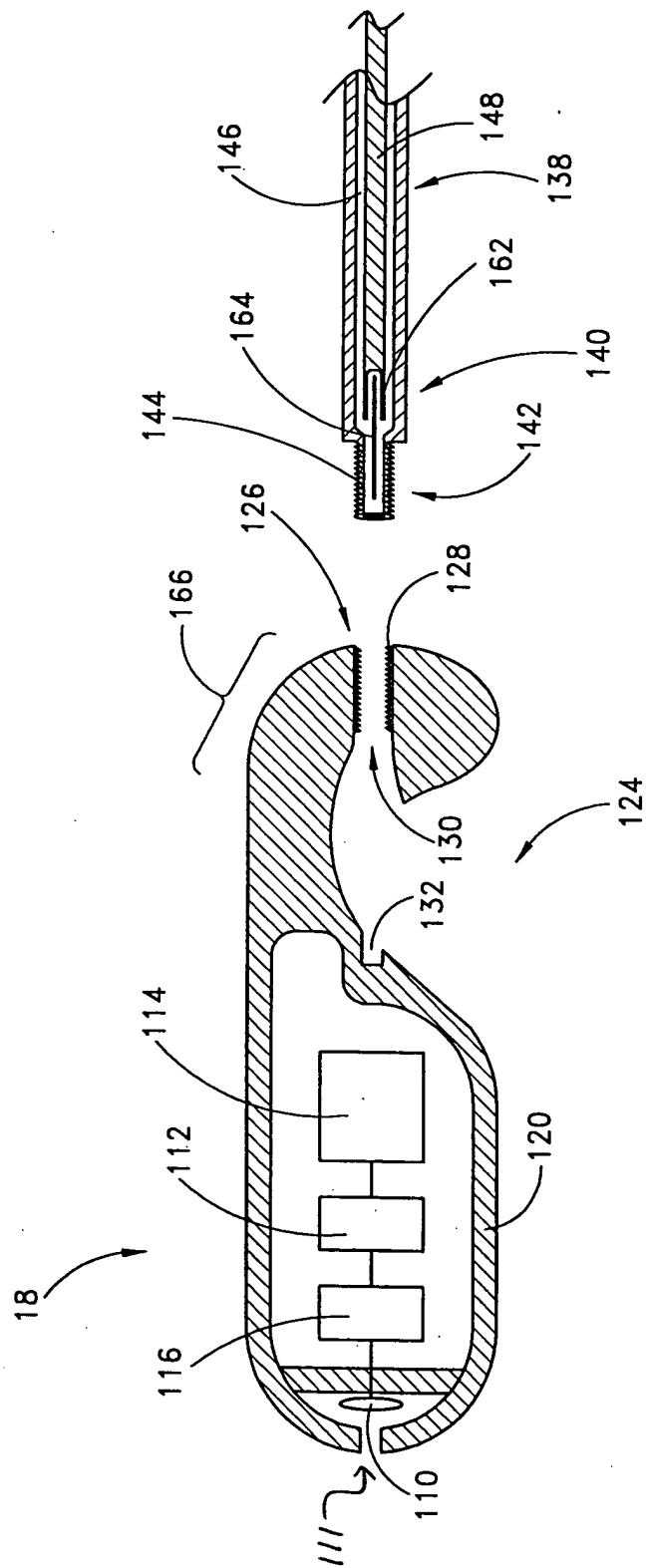
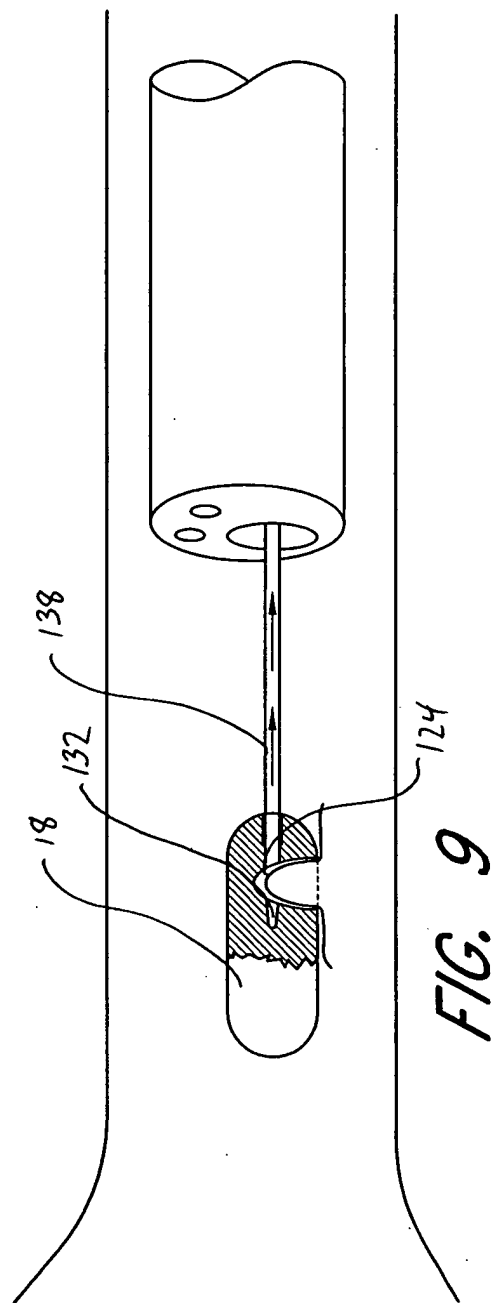
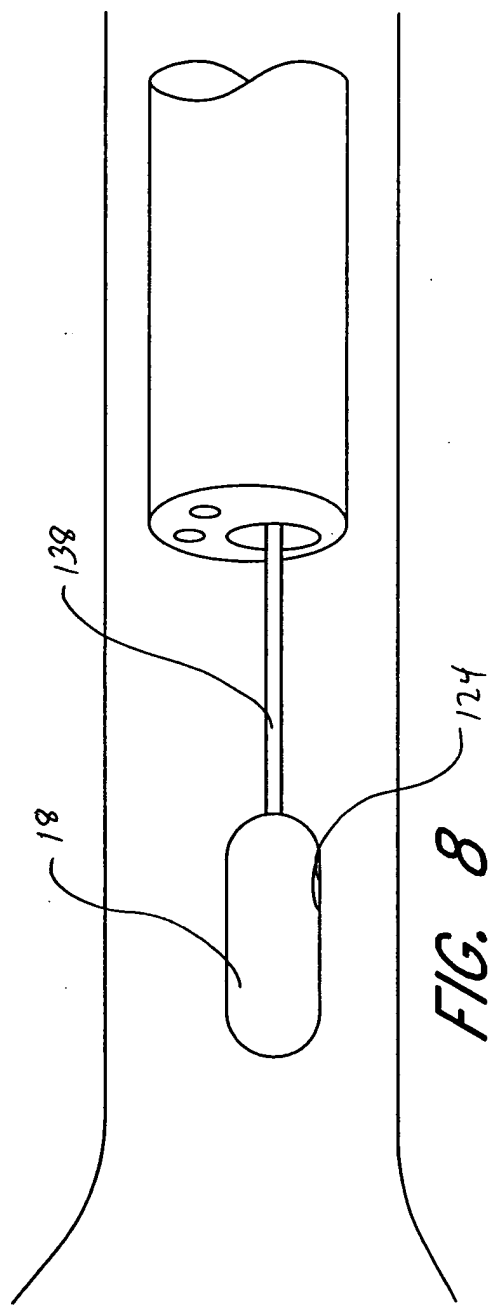
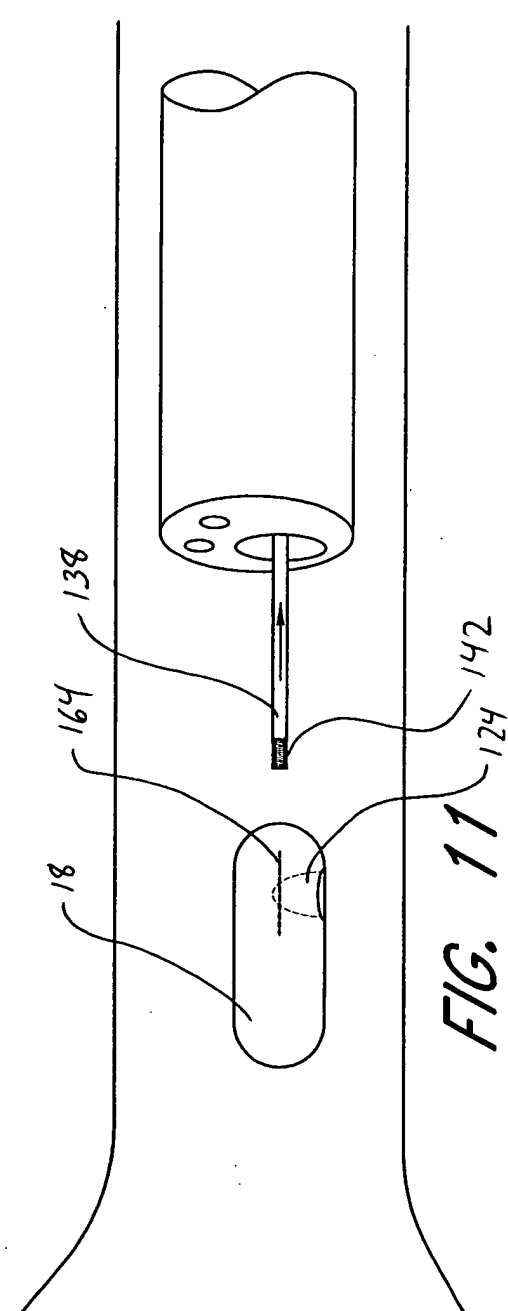
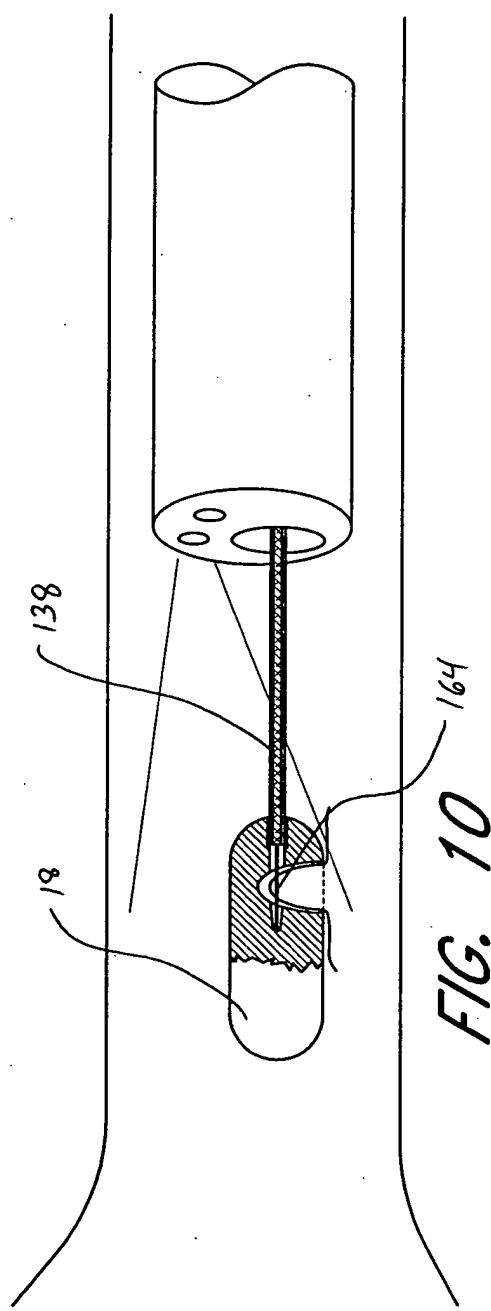
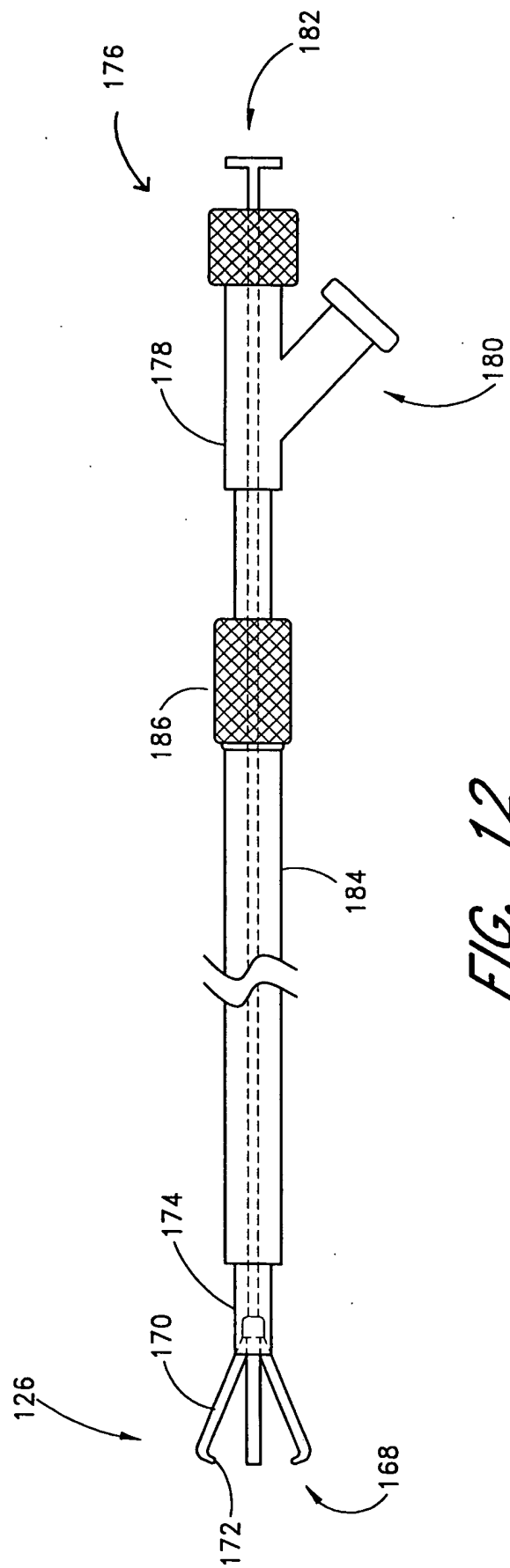


FIG. 7







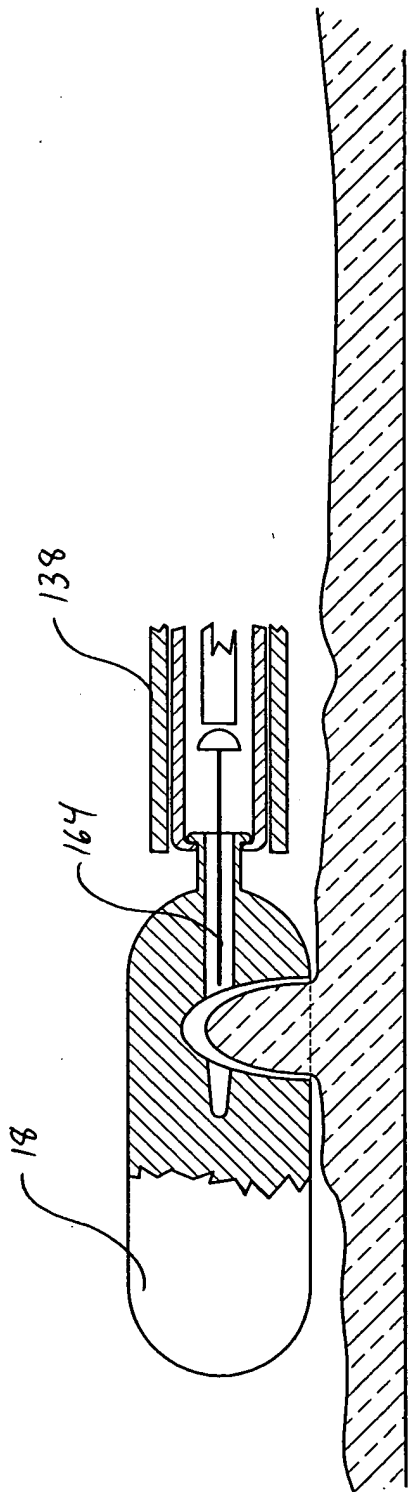


FIG. 13

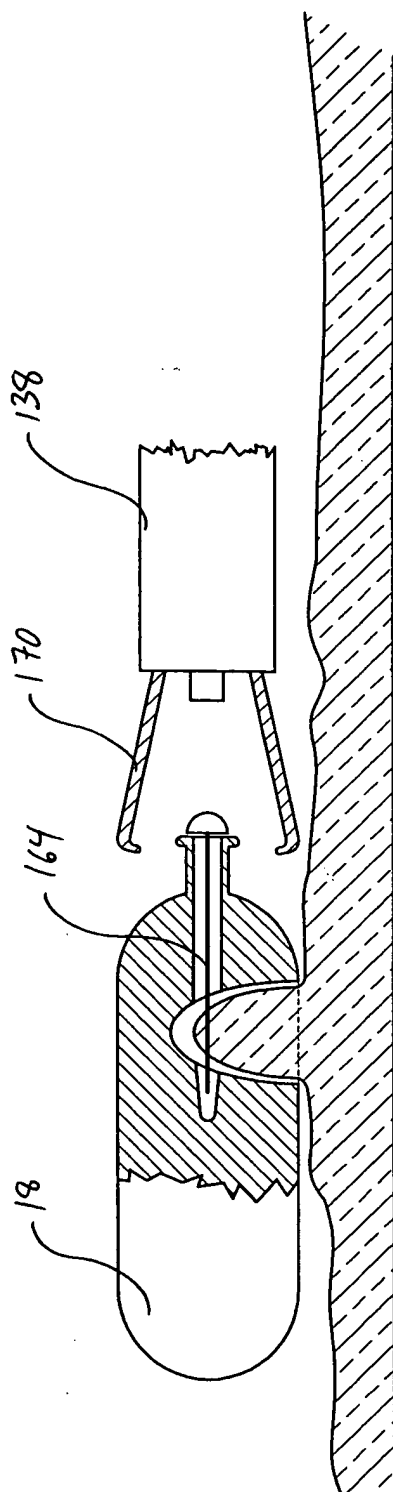


FIG. 14

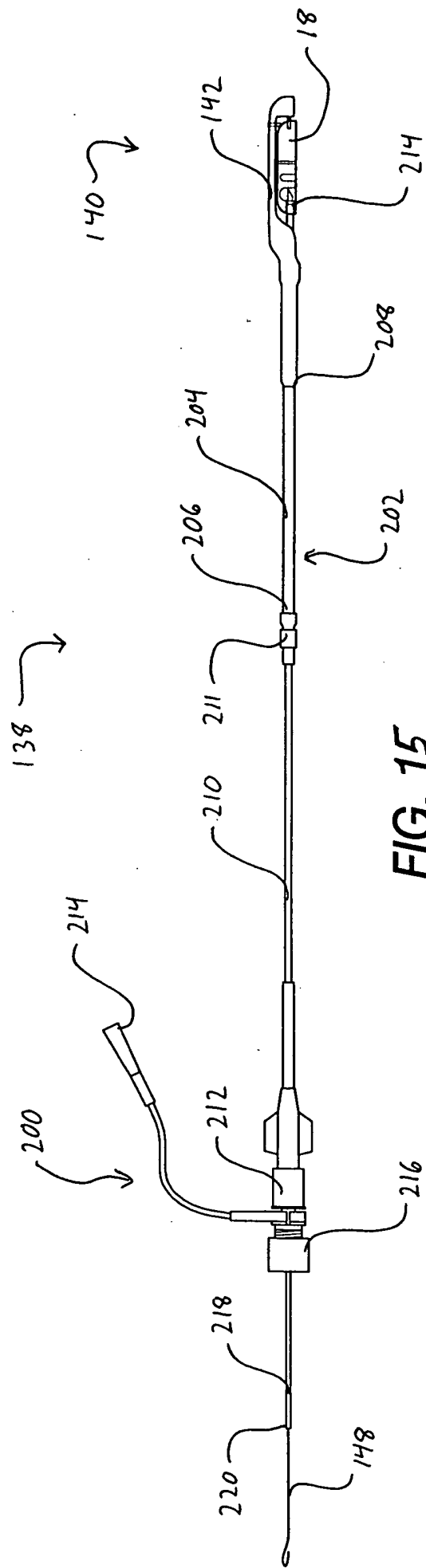
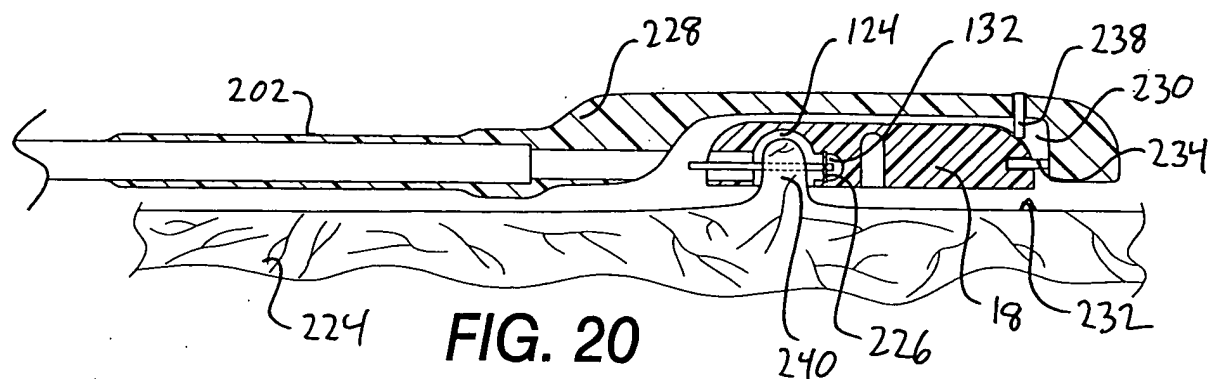
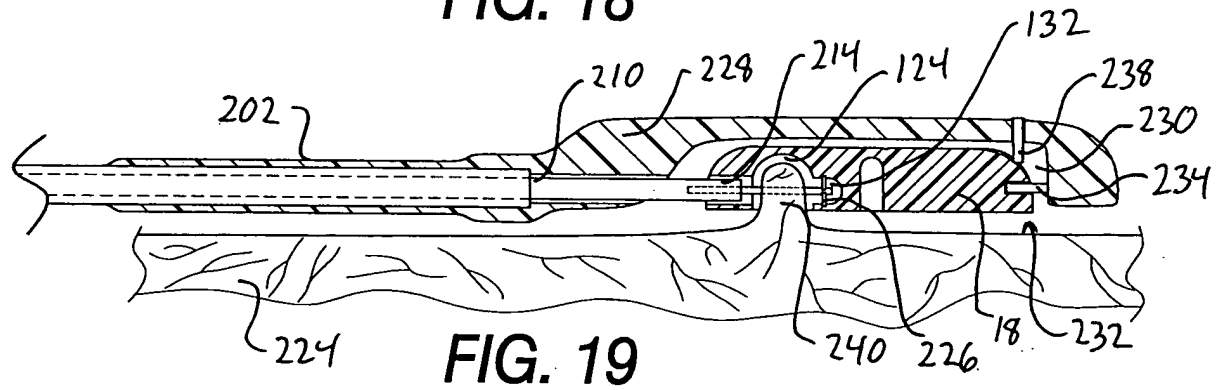
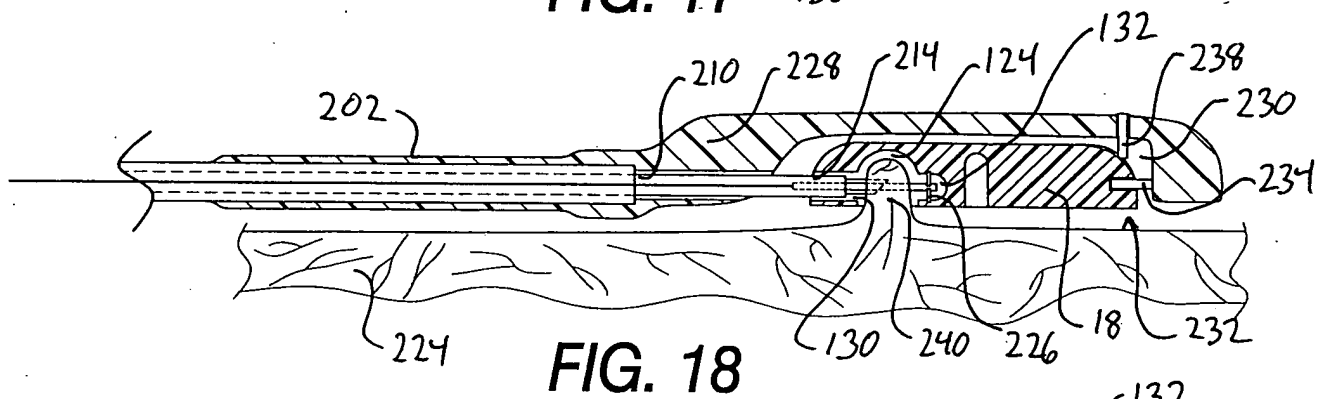
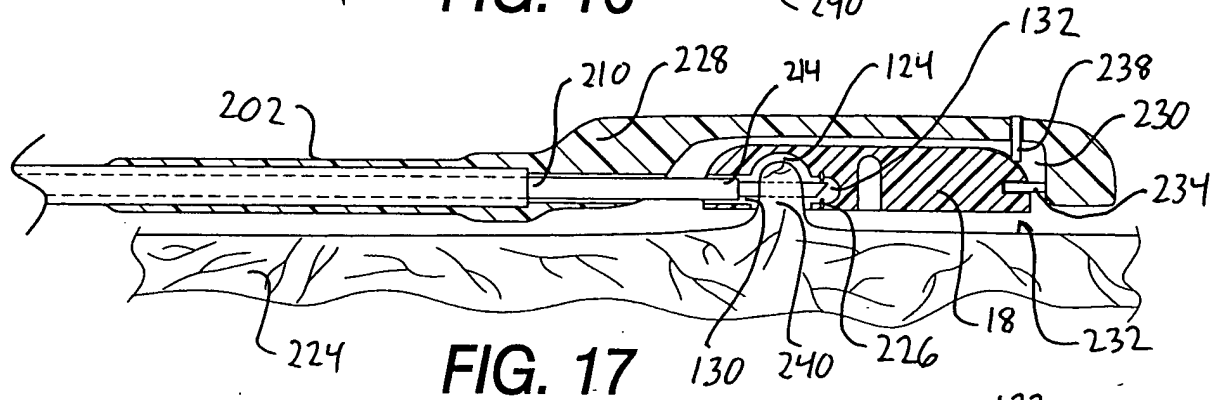
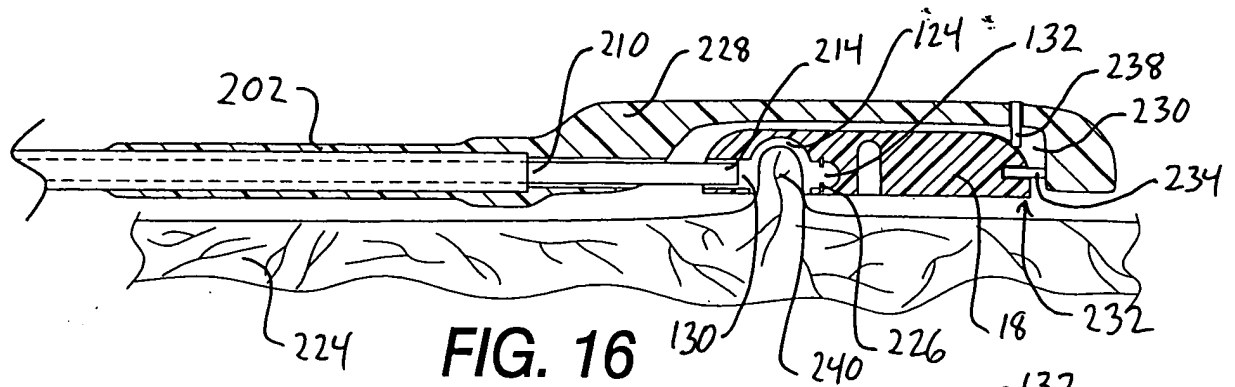
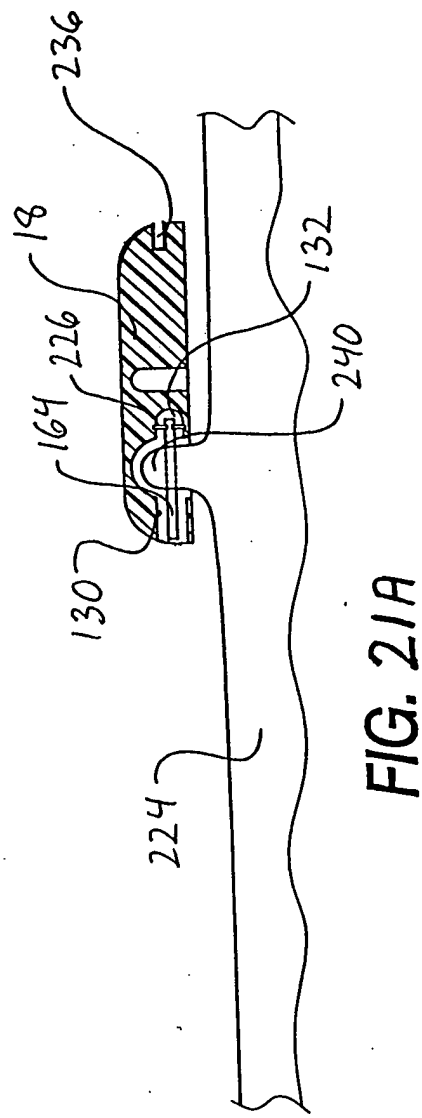
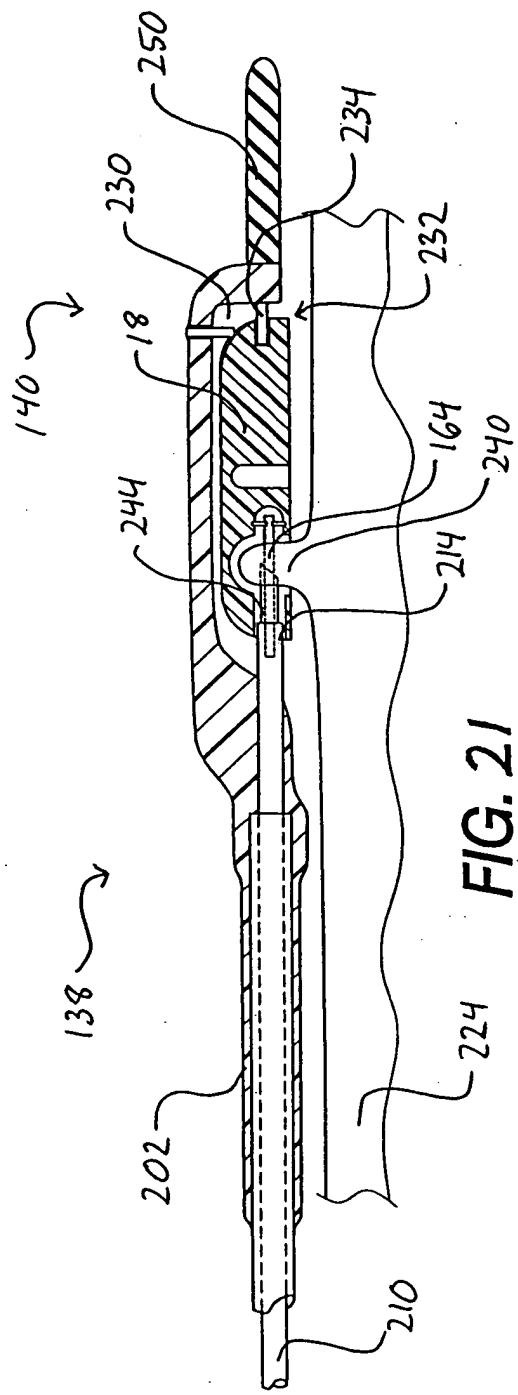
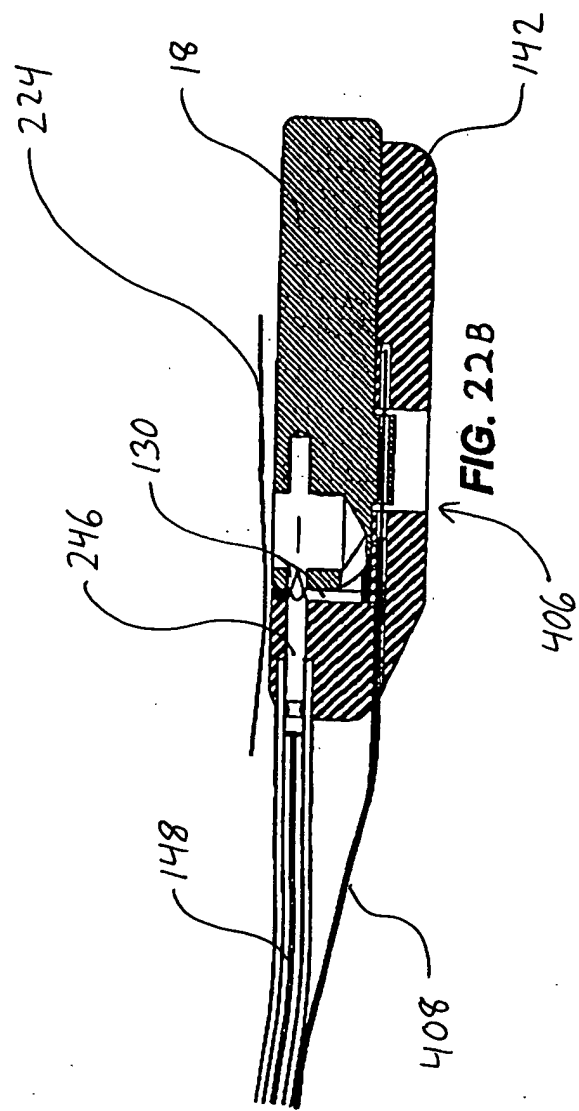
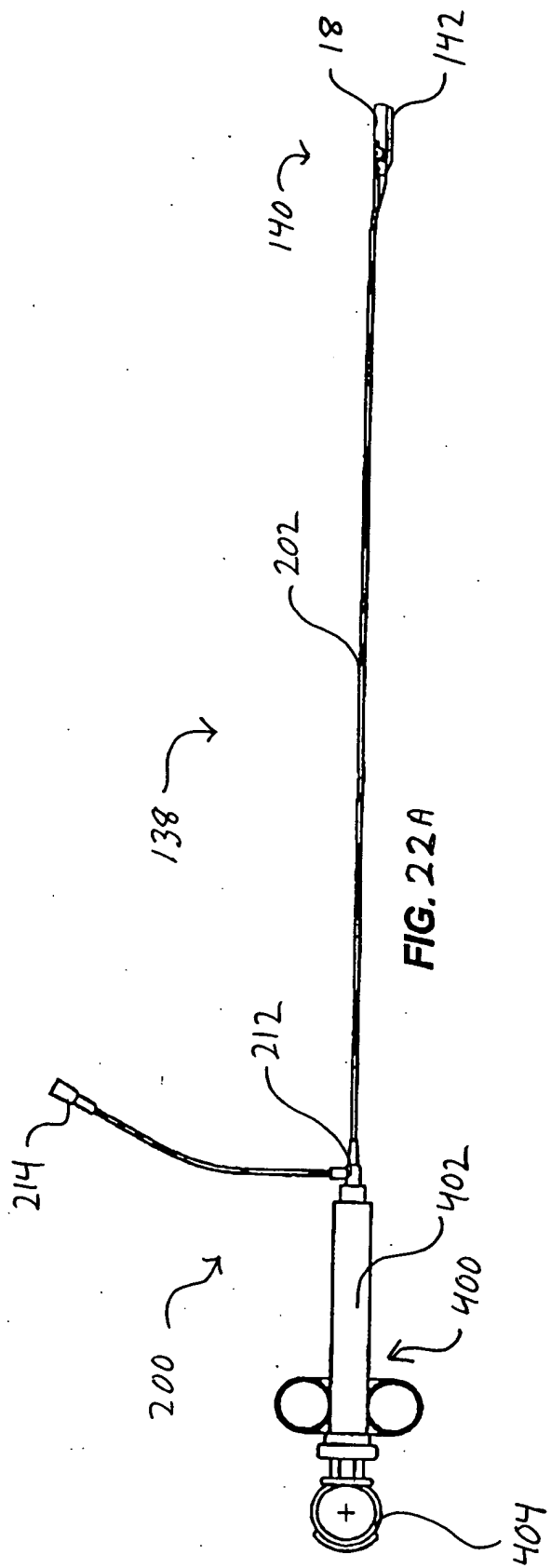
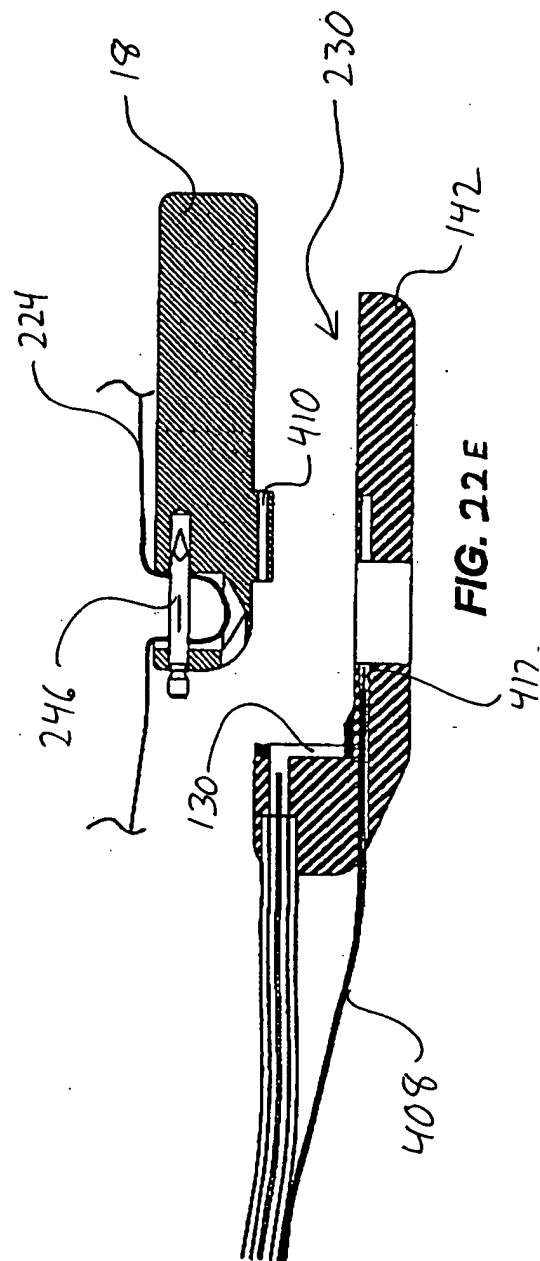
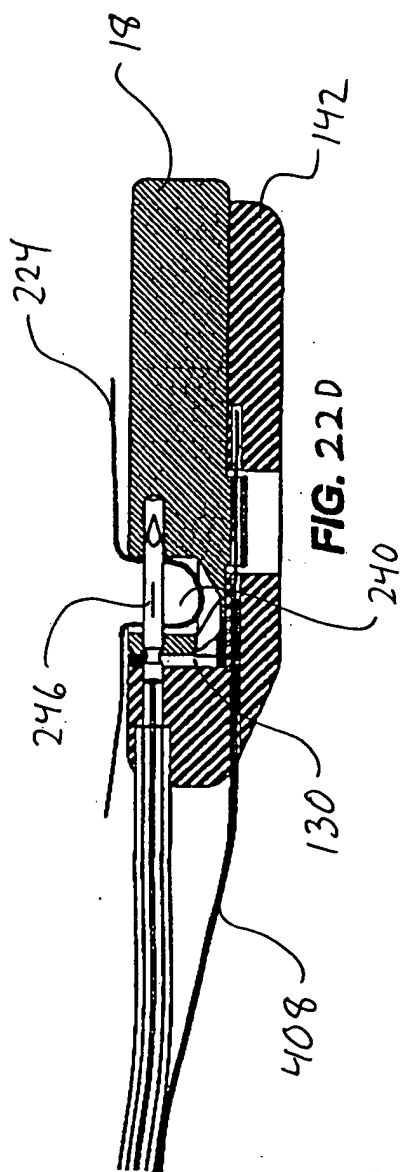
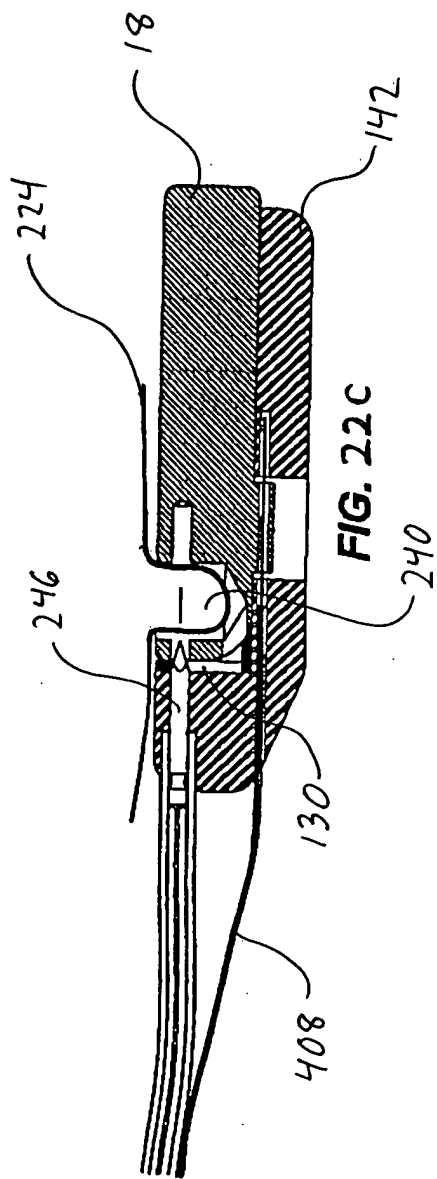


FIG. 15









NOTES

UNLESS NOTED OTHERWISE

1. A. RESISTOR VALUES IN OHMS

2. A. RESISTORS 0402 SIZE SURFACE MOUNT

3. A. NON-POLARIZED CAPACITORS <100pF 0402 SIZE SMT CERAMIC CHIP

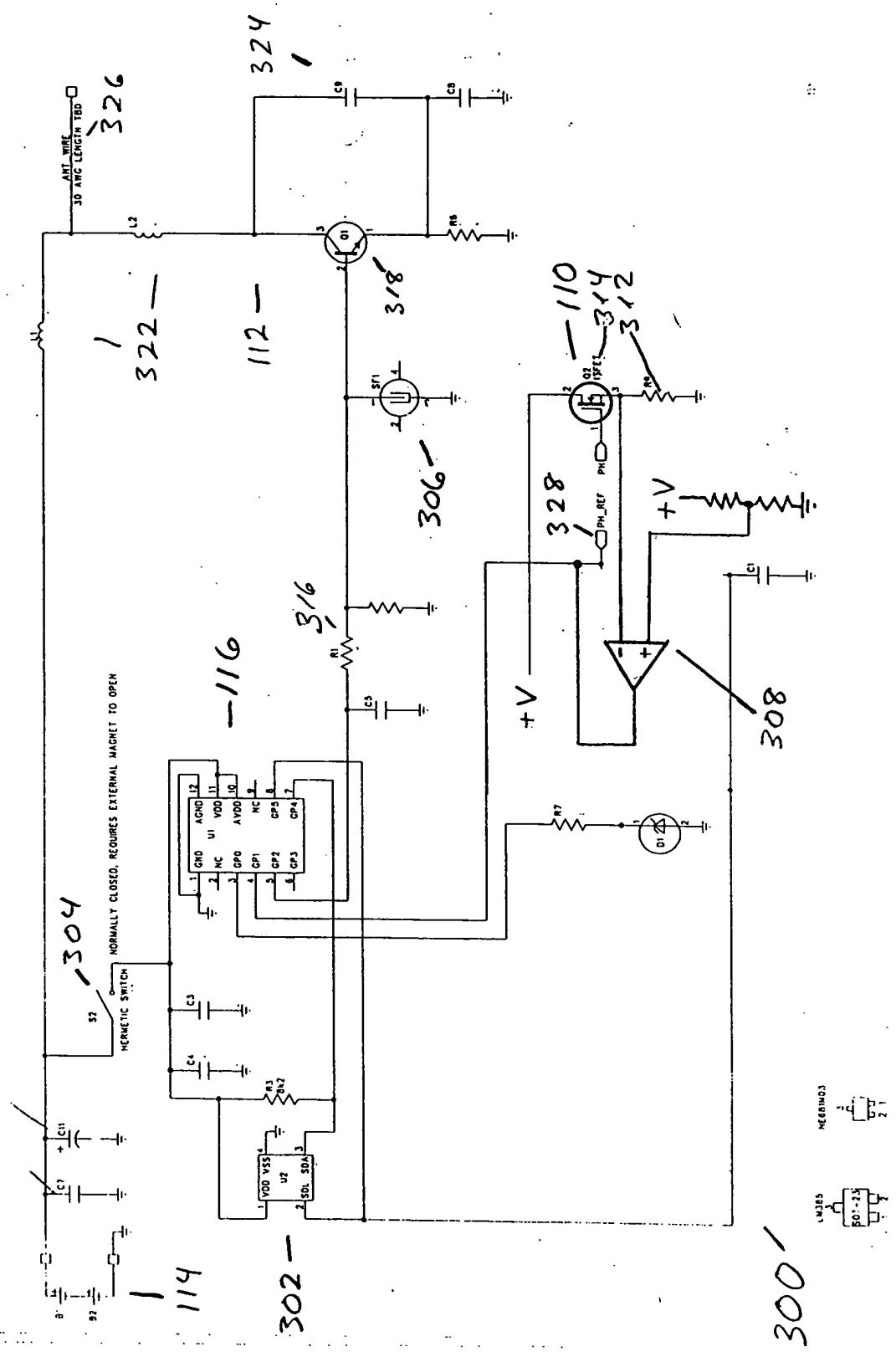


Figure 23

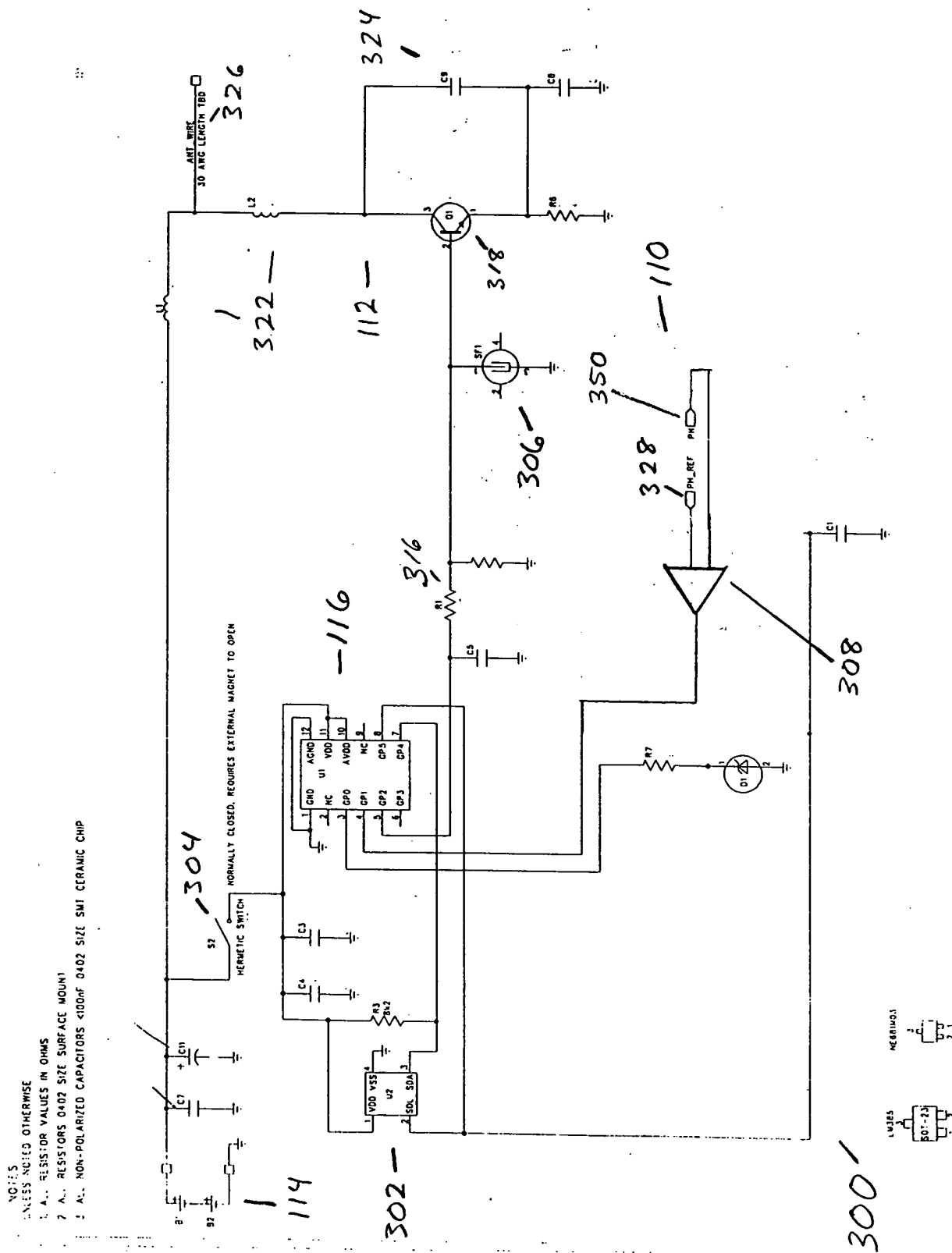


Figure 24

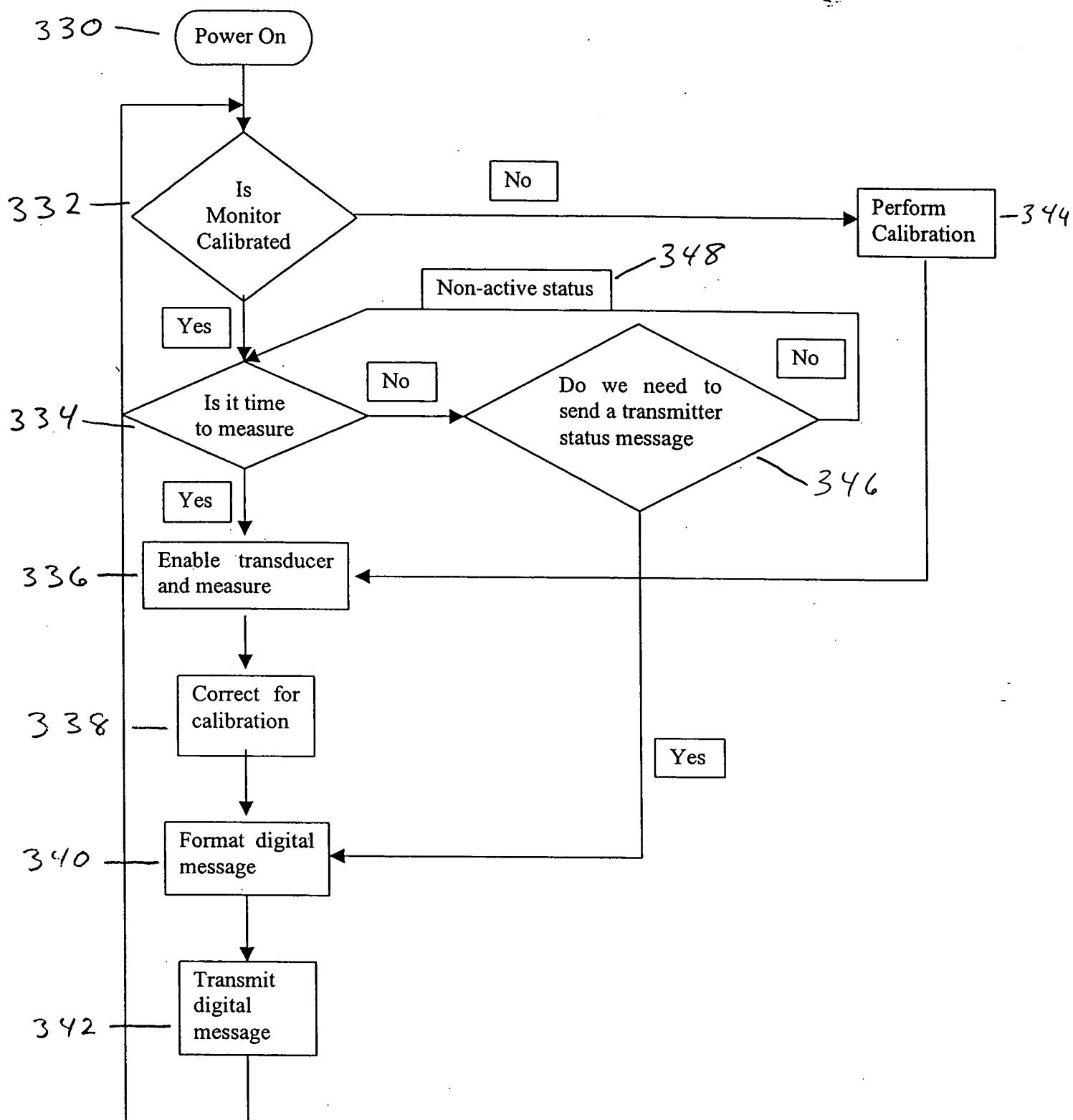


Figure 25

Message Structure

Preamble	Header		Payload	Trailer
	Transmitter ID	Message ID	Data	Checksum

Figure 26